

FIG. 1

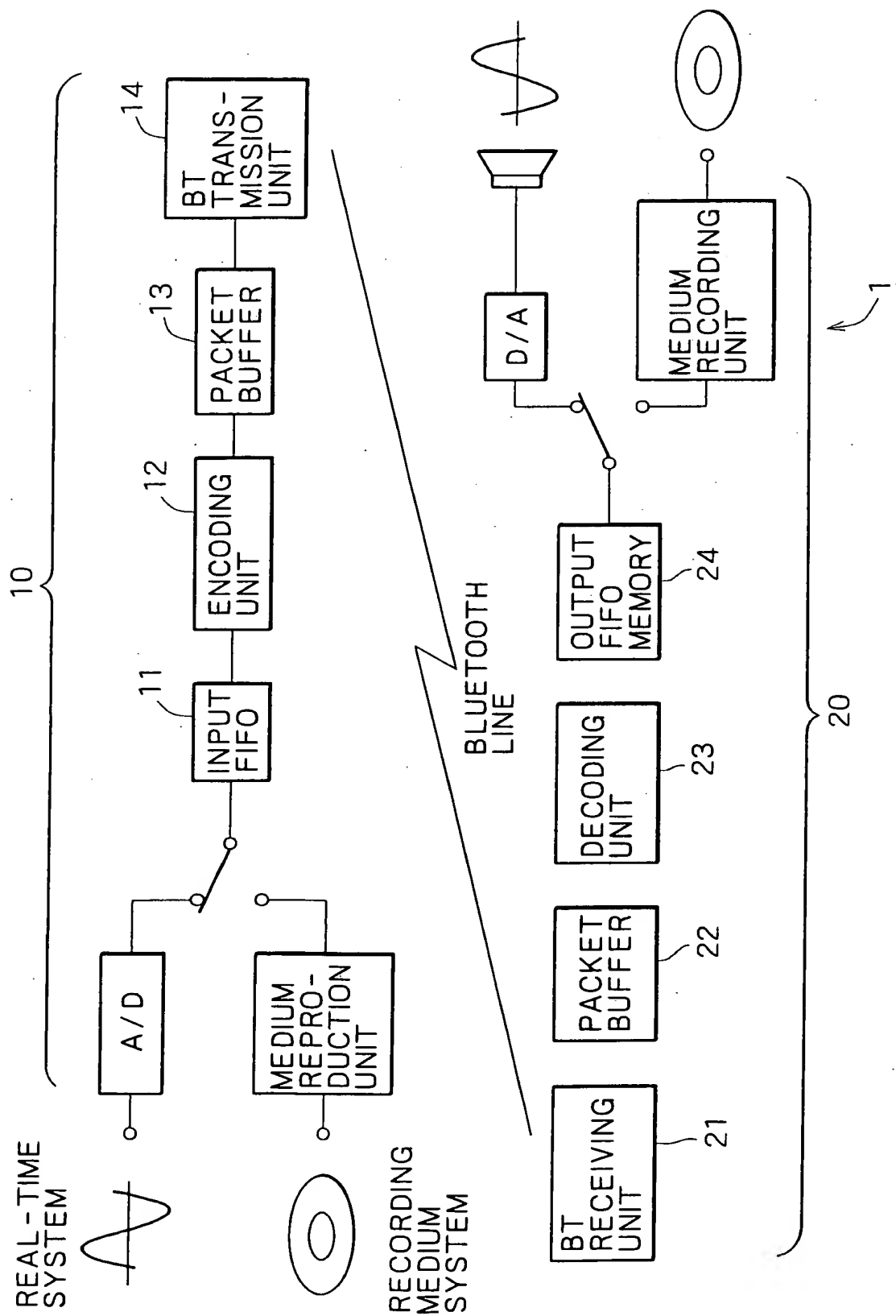


FIG. 2

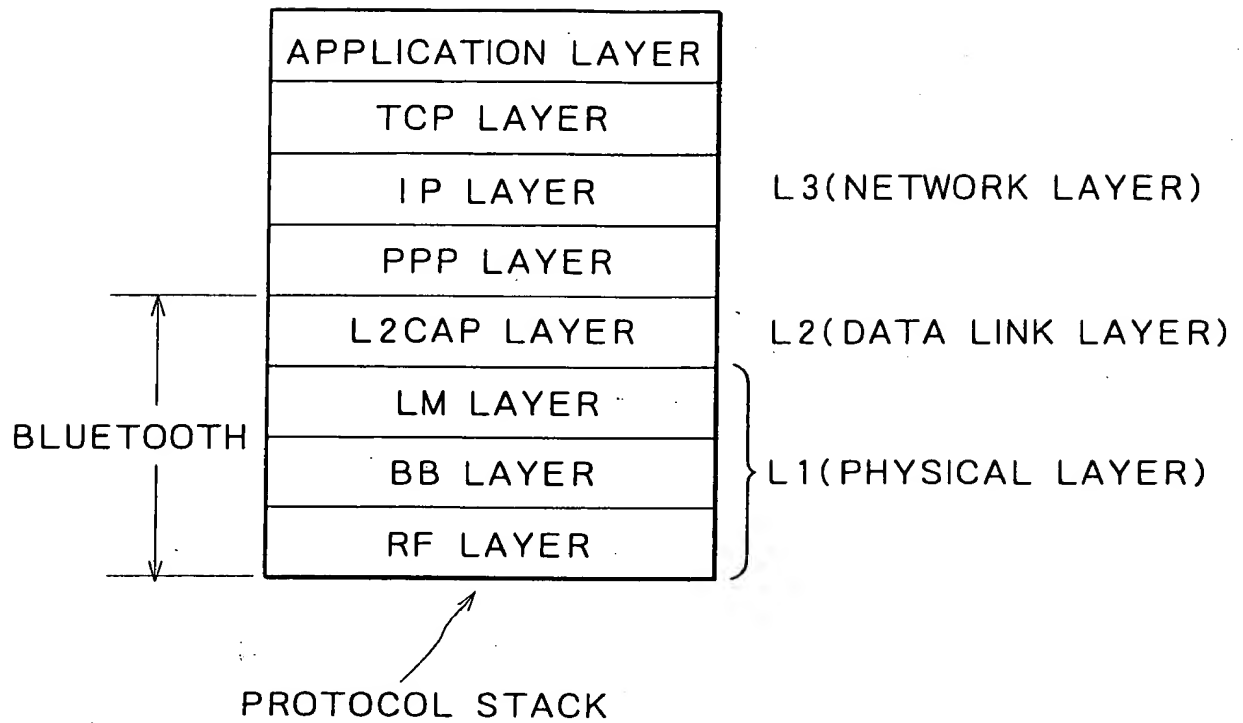
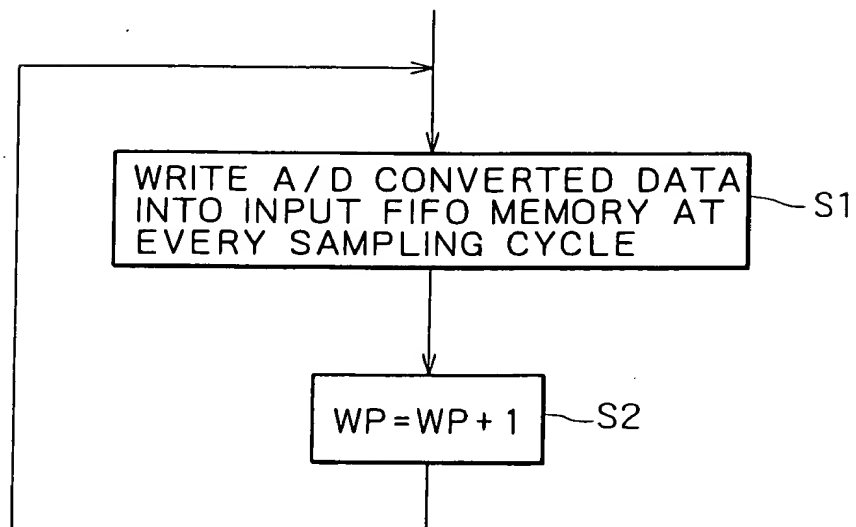
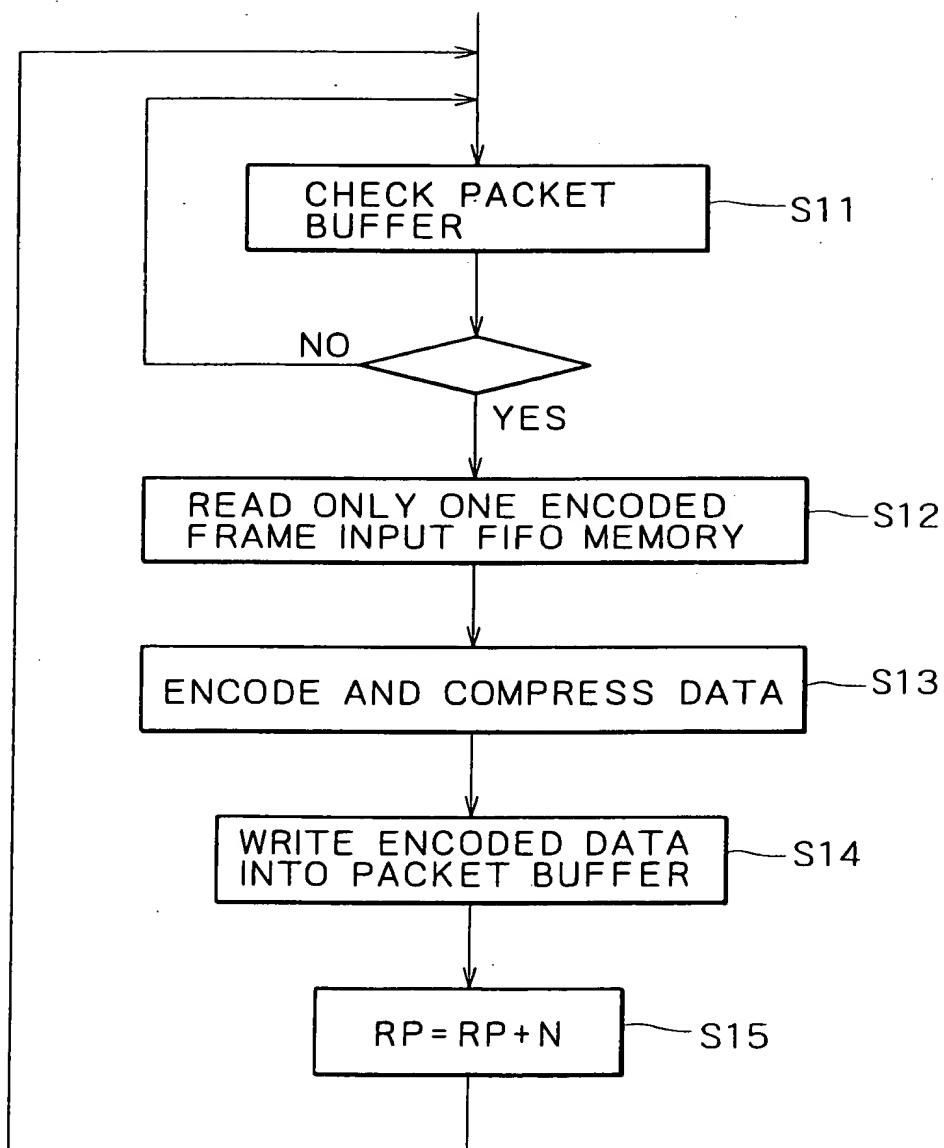


FIG. 3



0940551.082701

FIG. 4



FD-280-15504660

FIG. 5

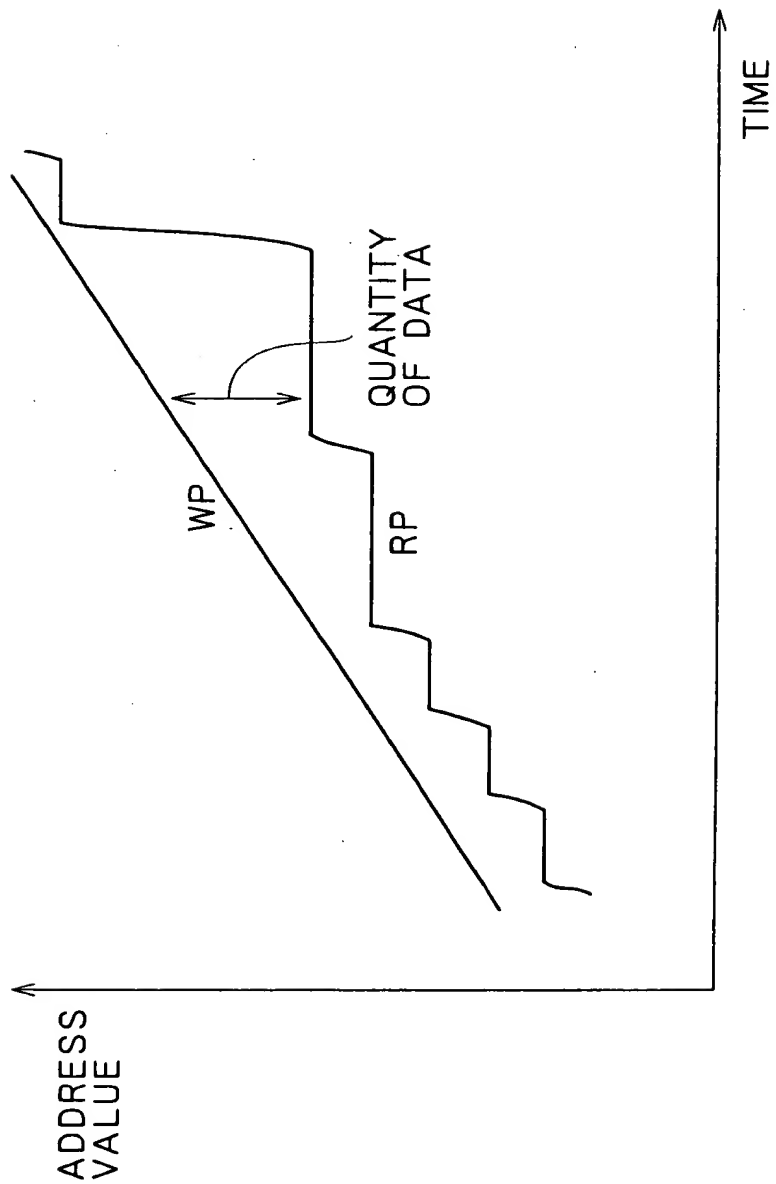
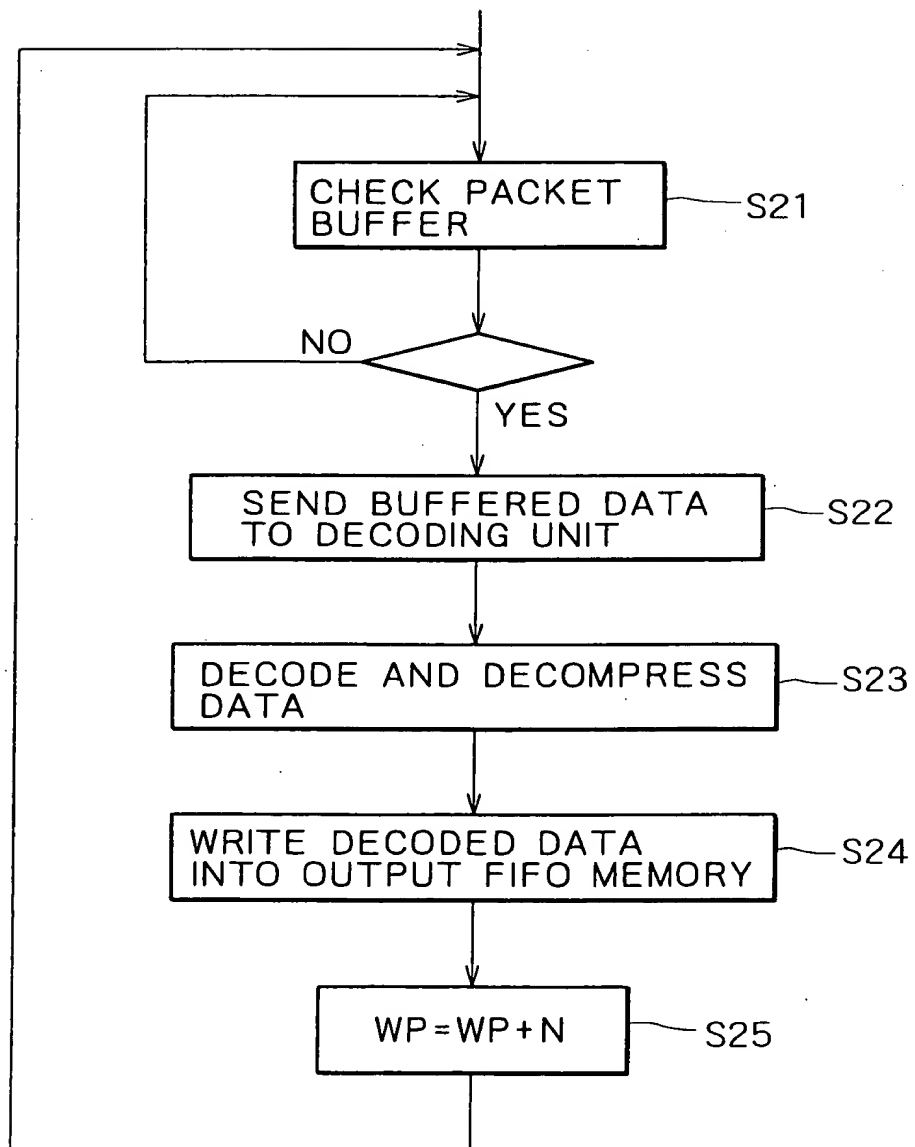


FIG. 6



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FIG. 7

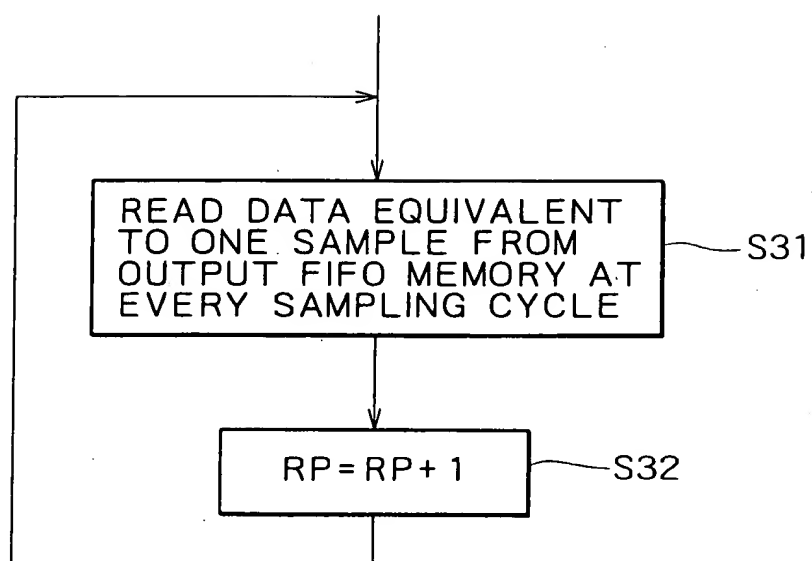
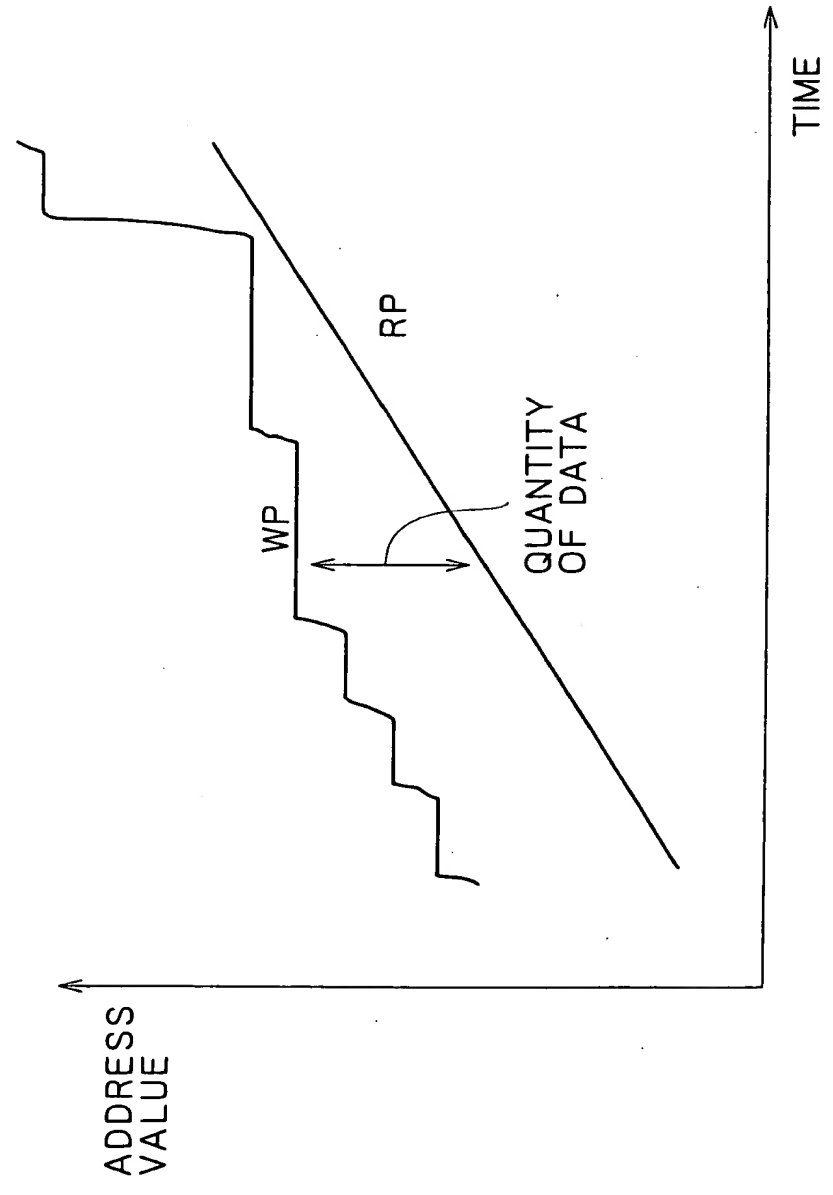
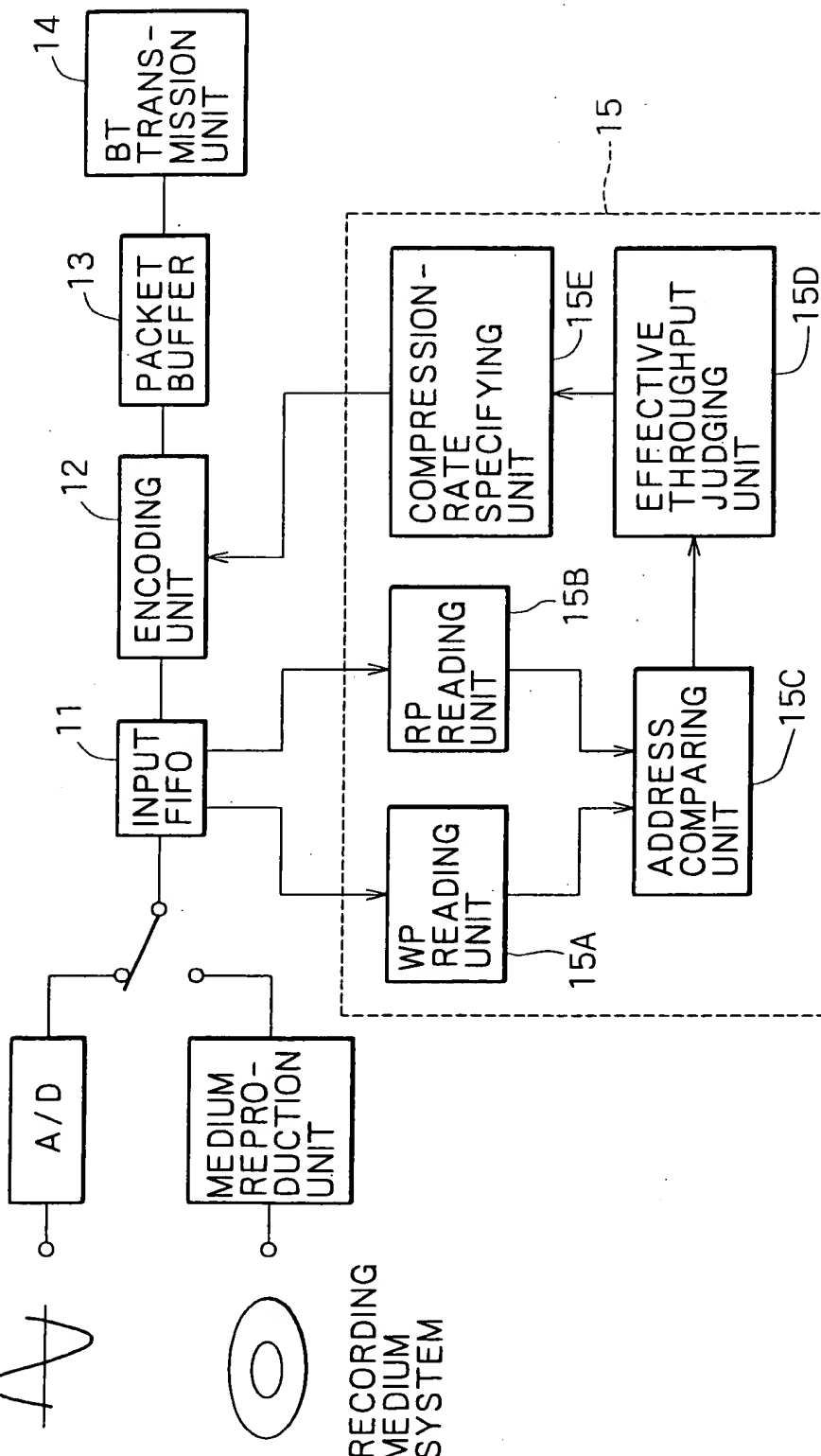


FIG. 8



REAL-TIME
SYSTEM



RECORDING
MEDIUM
SYSTEM



FIG. 10

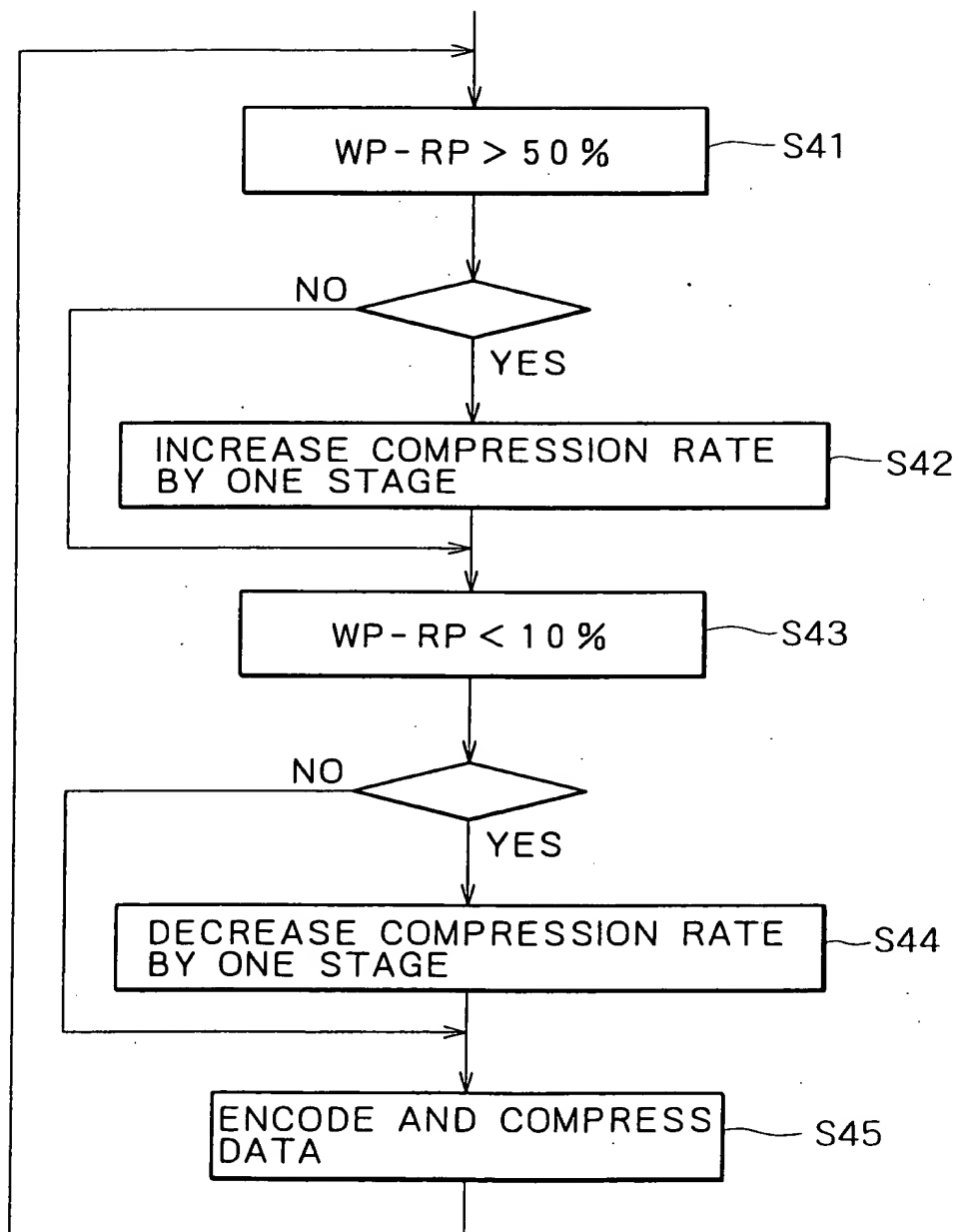
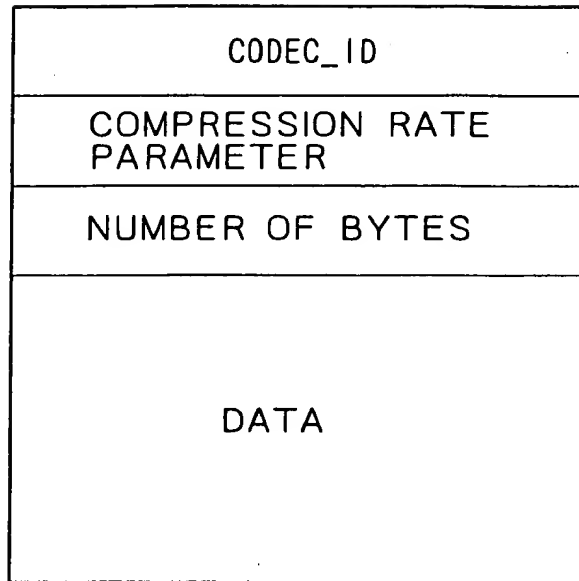


FIG. 11



DATA FRAME

FIG. 12

《 DATA CHANNEL 》

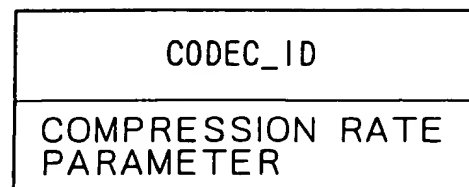
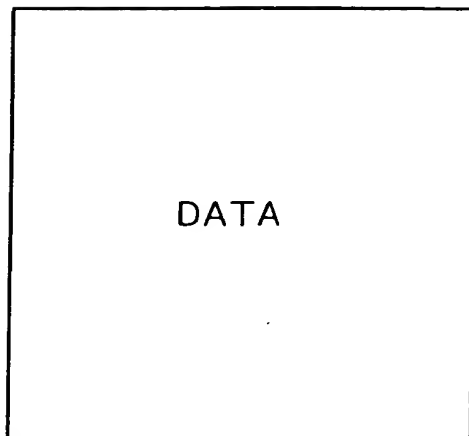


FIG. 13

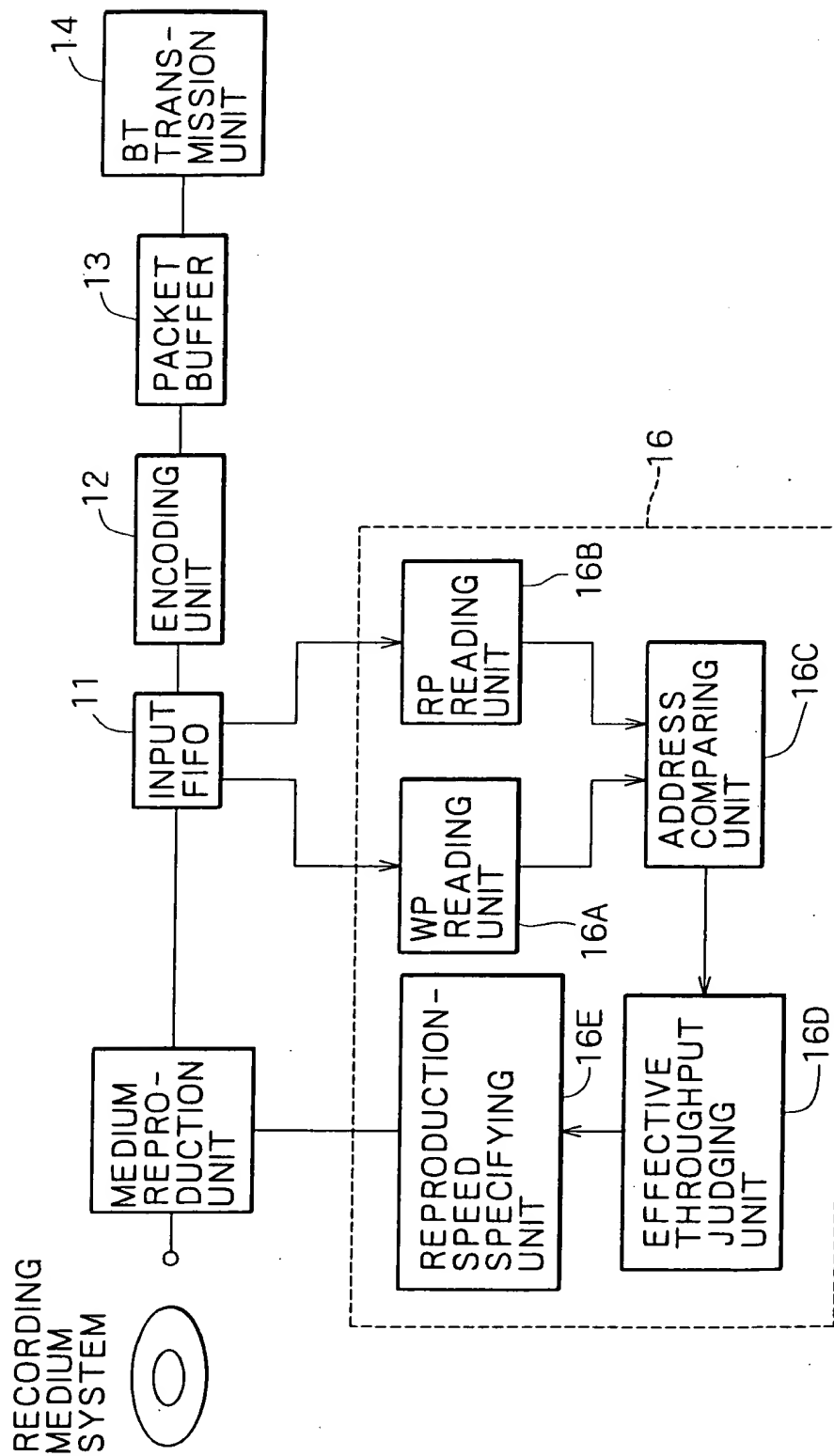


FIG. 14

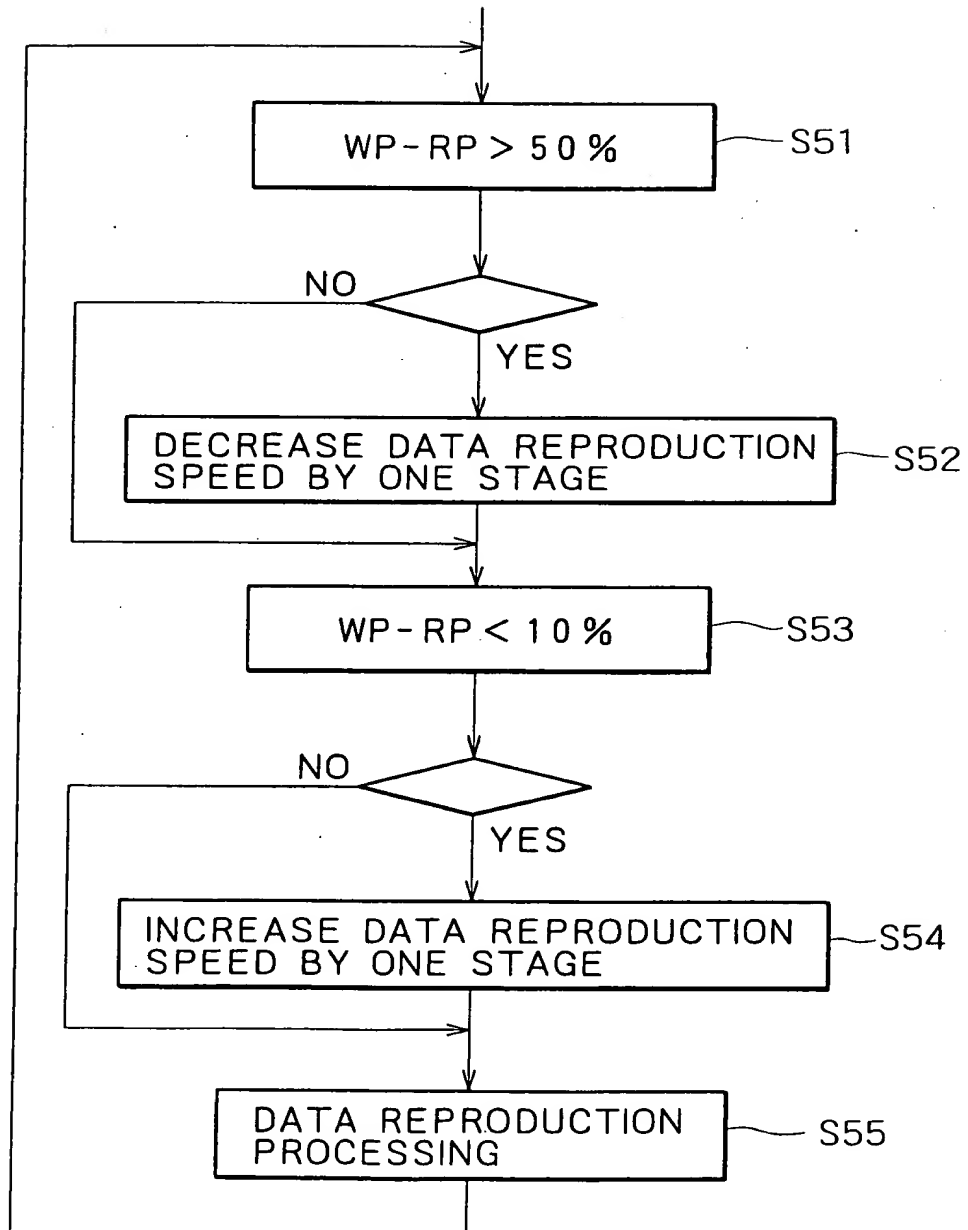


FIG. 15

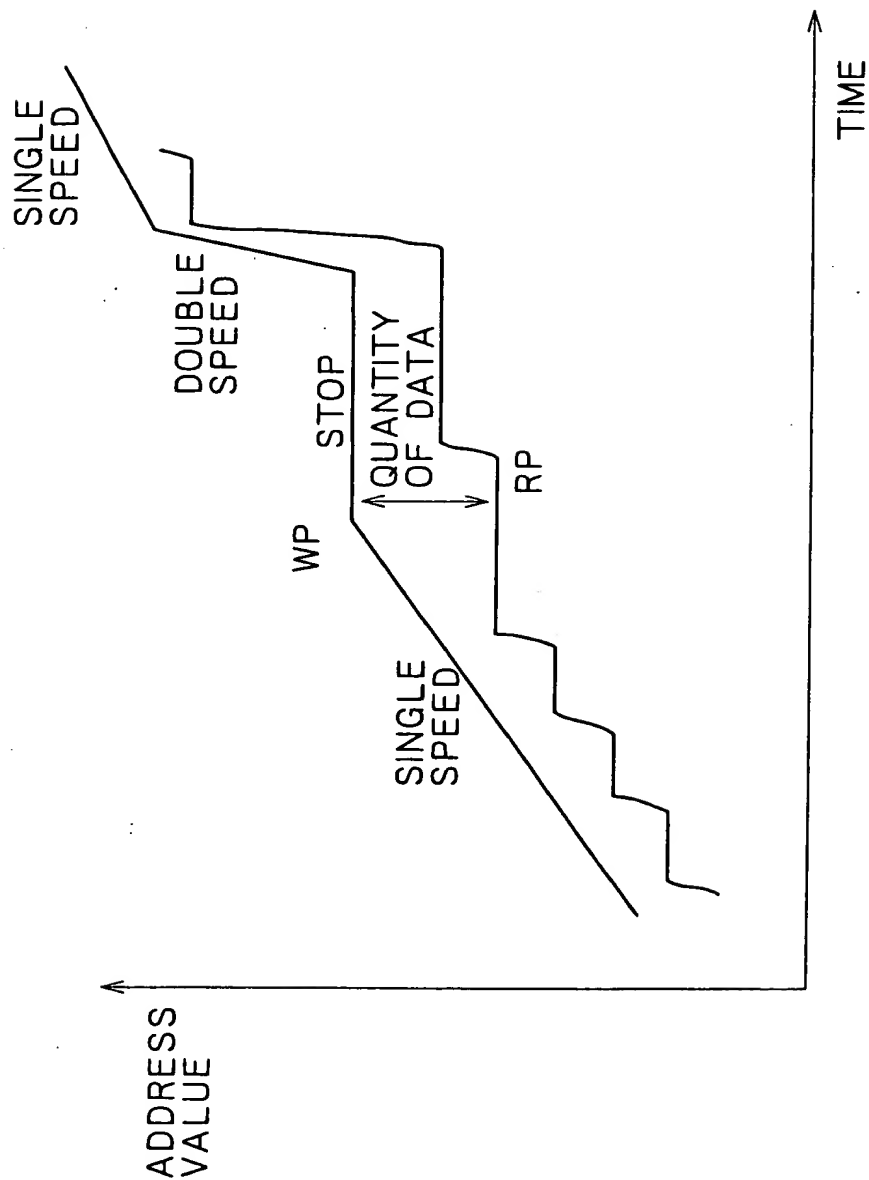


FIG. 16

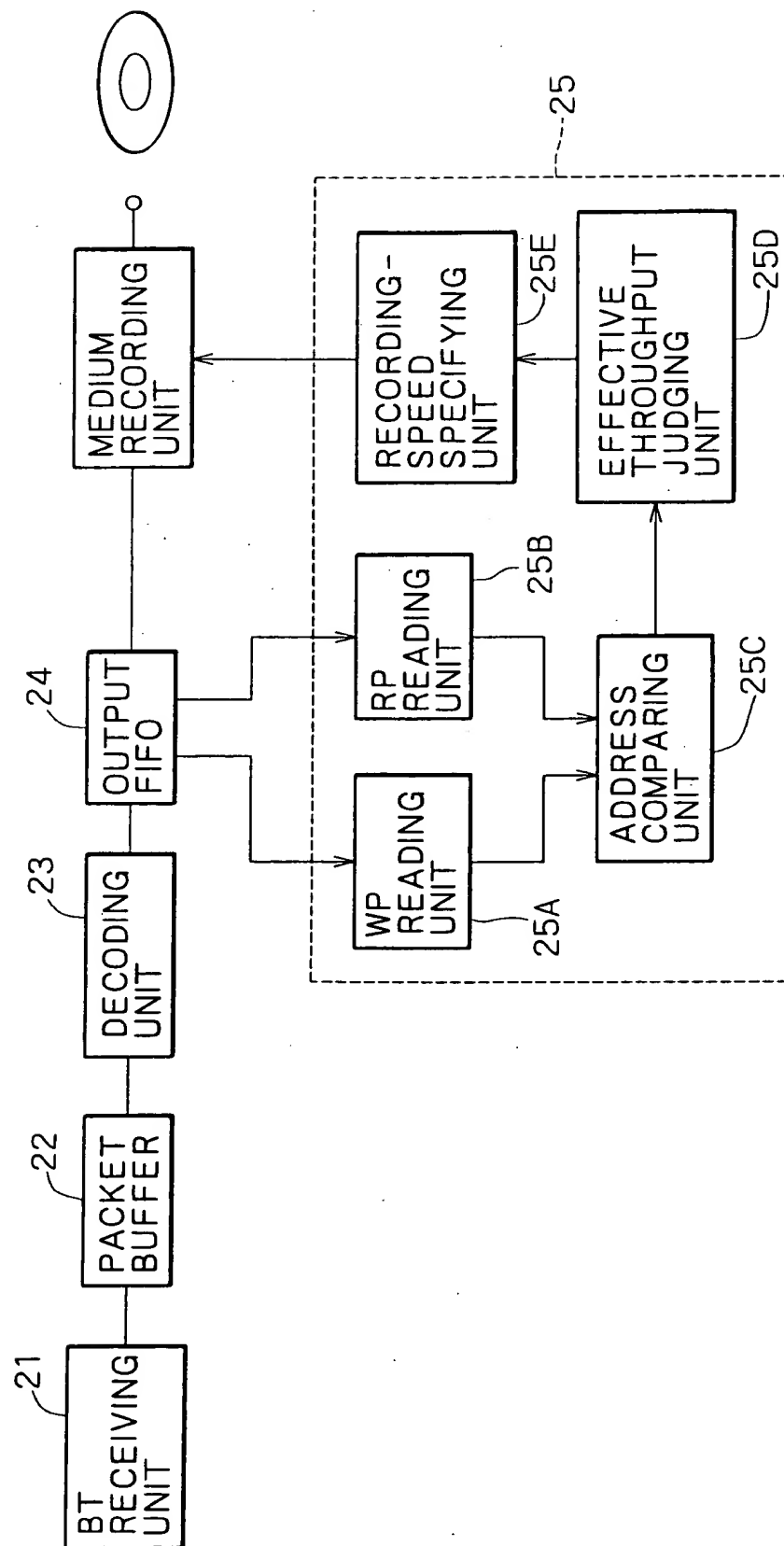
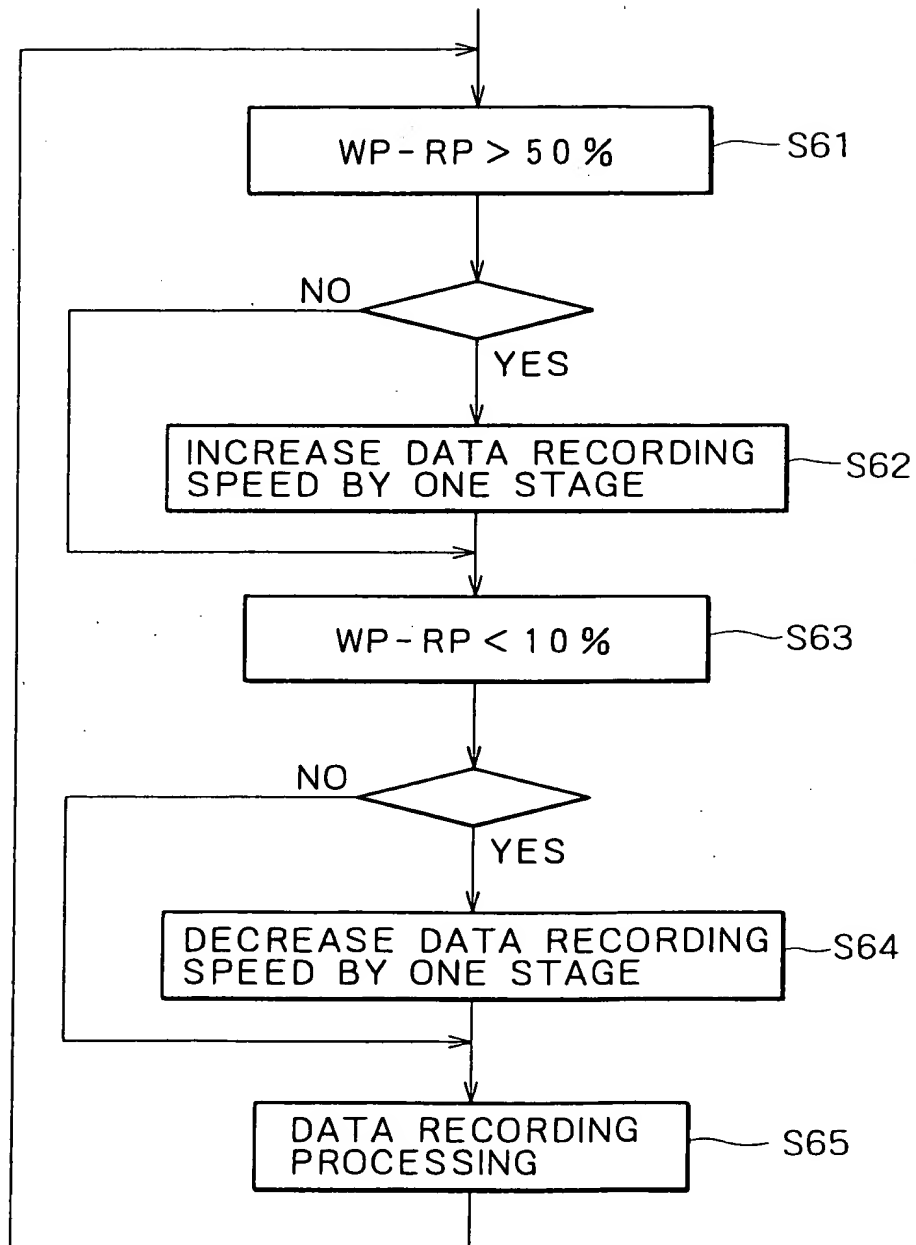


FIG. 17



0940551.0820

FIG. 18

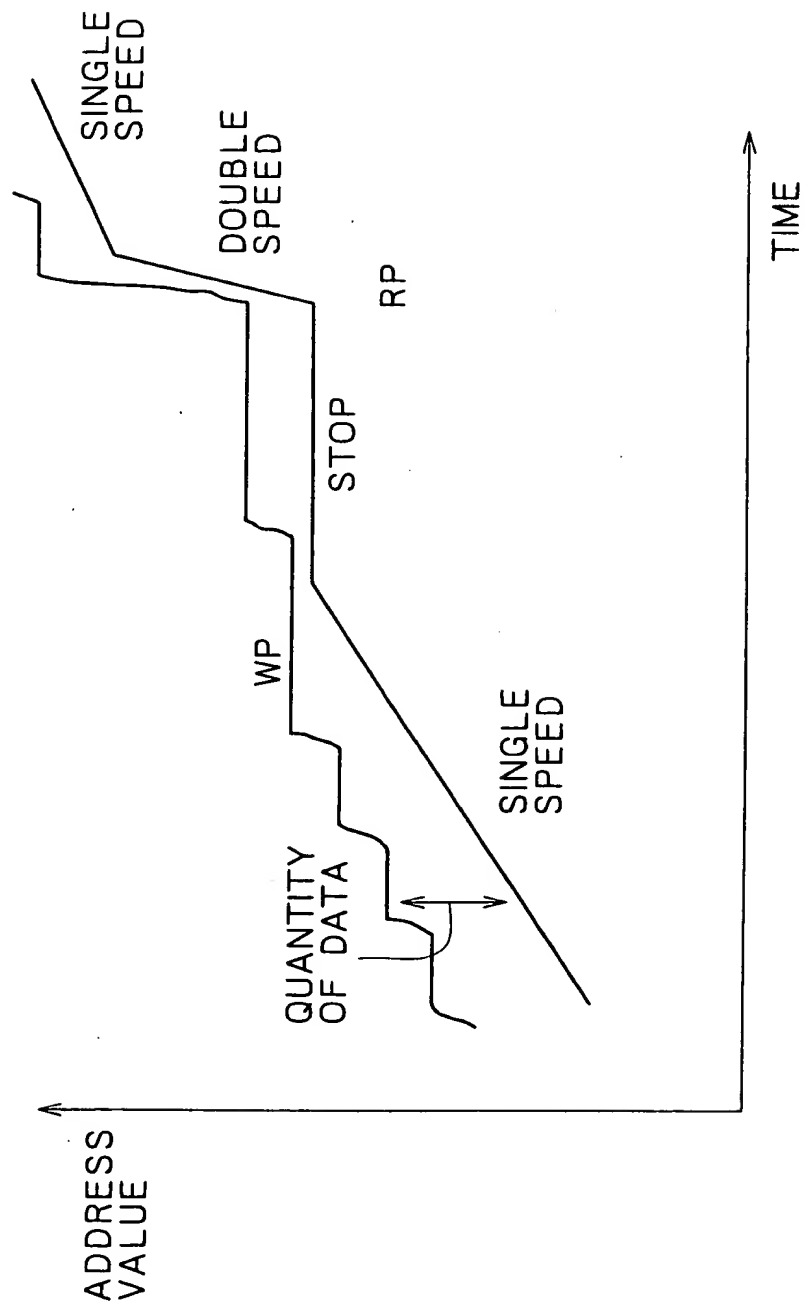
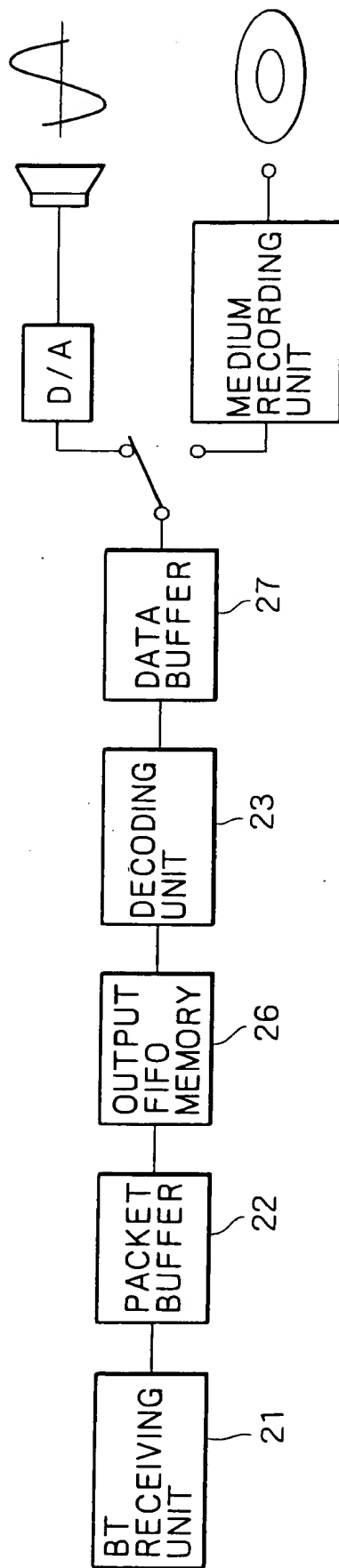


FIG. 19



20-3

0940551 0207280 T5504660

FIG. 20

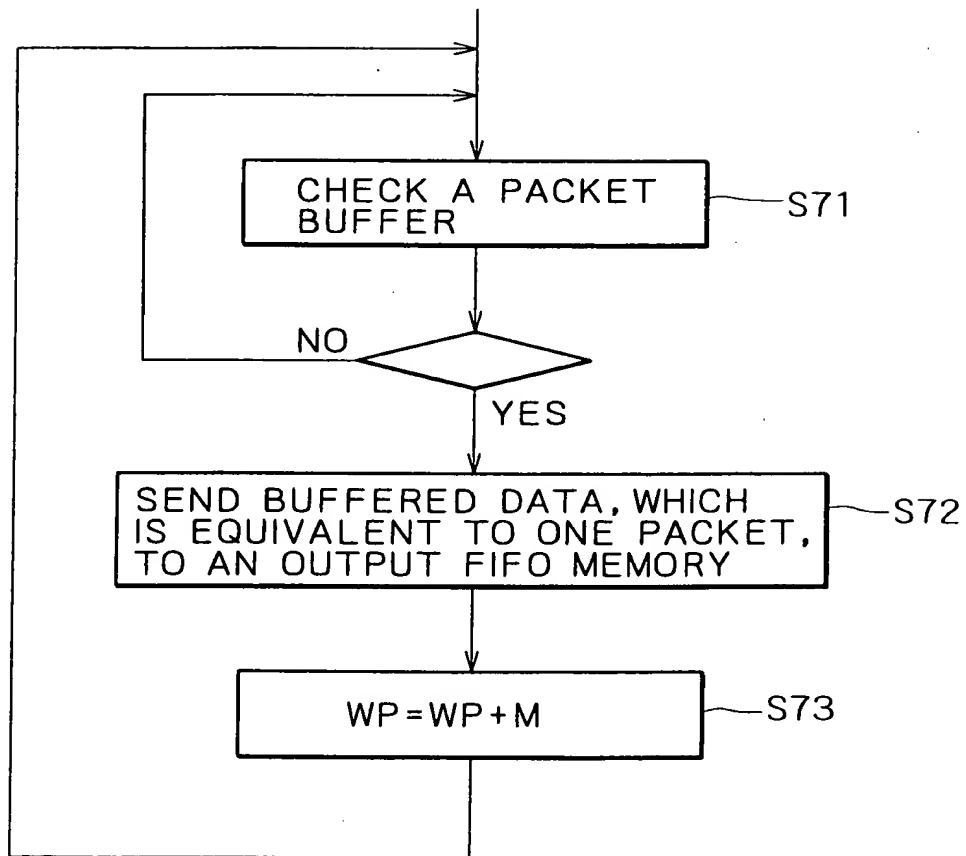
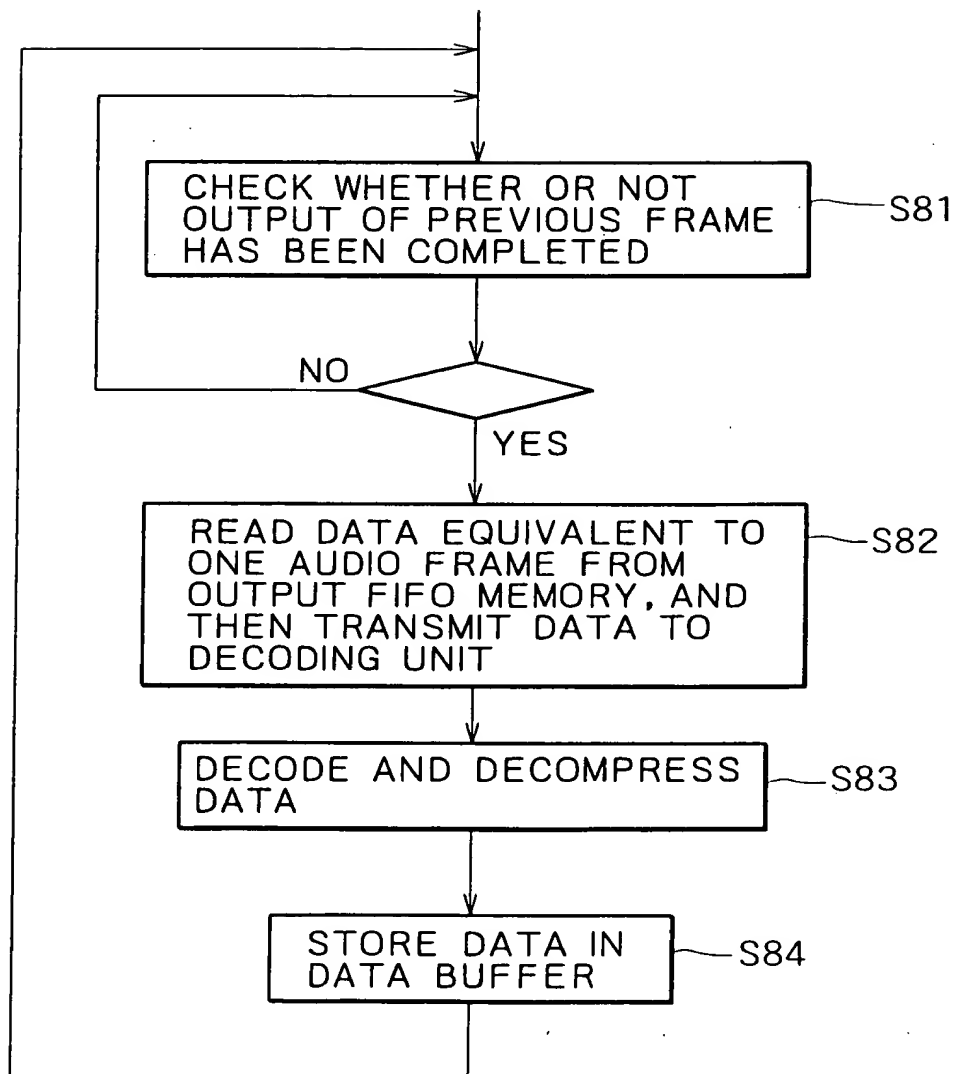


FIG. 21



TO 280" F550H660

FIG. 22A

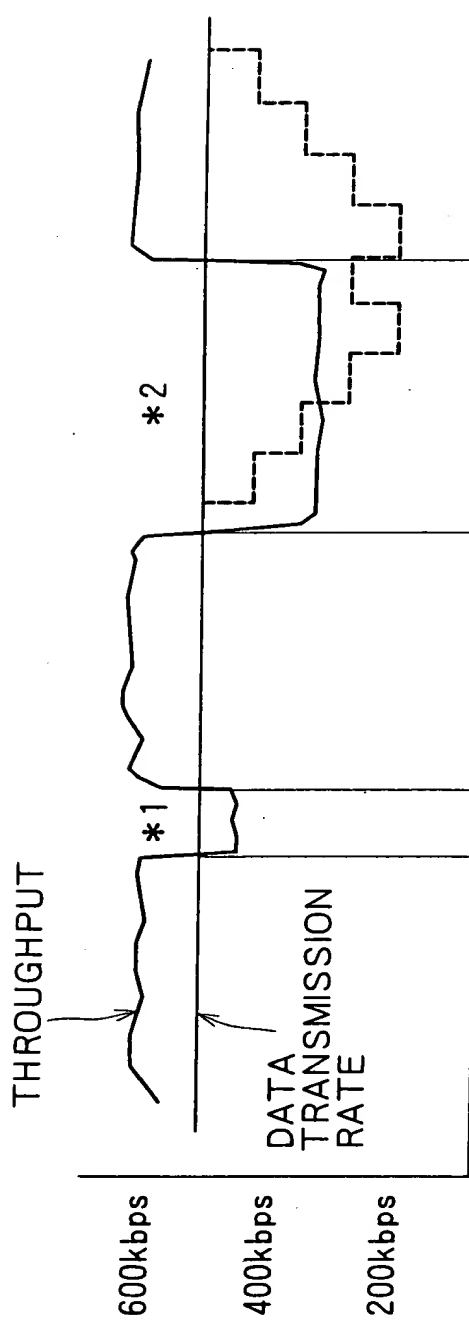


FIG. 22B

INPUT FIFO
MEMORY

FIG. 22C

OUTPUT FIFO
MEMORY

FIG. 22D

